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APPLICANTS

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This application is a 371 of PCT/JP04/09059 06/21/2004

** FOREIGN APPLICATIONS *****

JAPAN 2003-177412 06/23/2003

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Foreign Priority claimed 35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input checked="" type="checkbox"/> Met after Vtan Treas	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged /VIBOL TAN/ Examiner's Signature			JAPAN	1	18	1

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TITLE

Semiconductor integrated circuit device

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